	Application No.	Applicant(s)
Notice of Allewahility	10/660,568	LIM ET AL.
Notice of Allowability	Examiner	Art Unit
	Son L. Mai	2827
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>papers filed 09-12-03</u> .		
2. The allowed claim(s) is/are <u>1-7</u> .		
3. A The drawings filed on 12 September 2003 are accepted by the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some* c) None of the: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements		
noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	MENT of this application.	
5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
 CORRECTED DRAWINGS (as "replacement sheets") mu (a) including changes required by the Notice of Draftsper to Paper No./Mail Date including changes required by the attached Examiner Paper No./Mail Date ldentifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in 	son's Patent Drawing Review (PTO 's Amendment / Comment or in the C 1.84(c)) should be written on the drawithe header according to 37 CFR 1.121(Office action of ngs in the front (not the back) of d).
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT	osit of BIOLOGICAL MATERIAL (FOR THE DEPOSIT OF BIOLOGIC	must be submitted. Note the AL MATERIAL.
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5. Notice of Informal F	Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary Paper No./Mail Da	
 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date 09-12-03 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	7. Examiner's Amend	ment/Comment ent of Reasons for Allowance
		Son Mai Patent Examiner Art Unit 2827

Application/Control Number: 10/660,568 Page 2

Art Unit: 2827

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

On page 1 of the specification, at line 5 of paragraph [0002], "havinga" has been changed to –having a--.

In claim 3, lines 12-13, the recitation "half voltage the memory cell array voltage" has been changed to –the half voltage of the memory cell array voltage--

Allowable Subject Matter

- 2. Claims 1-7 are allowed.
- 3. The following is an examiner's statement of reasons for allowance: The prior art of record fails to teach or suggest a precharge circuit and method in a semiconductor memory device having a hierarchical structure in which bitline pairs are connected with local input/output line pairs, and the local input/output line pairs are connected to global input/output line pairs. In particular, a method comprises: precharging the global input/output line pairs with the half of a memory cell array voltage; and precharging the local input/output line pairs with the half of the memory cell array voltage.
- 4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

Art Unit: 2827

accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Yamauchi; Hiroyuki (U.S. Patent US 5642314), Sakurai; Mikio et al. (U.S. Patent US 5650980), Arimoto; Kazutami (U.S. Patent US 5946252), and Park; San-Ha (U.S. Patent US 6771550) teach precharge circuits and methods for precharging bitlines, local input/output line pairs and global input/output line pairs.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Son L. Mai whose telephone number is 571-272-1786.

The examiner can normally be reached on 8am to 6pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hoai Ho can be reached on 571-272-1777. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

04-01-05

Son L. Mai Primary Examiner Art Unit 2827